Notice of Allowability	Application No.	Applicant(s)
	10/624,506	BYEON ET AL.
	Examiner	Art Unit
	Edward Wojciechowicz	2815
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not included
1. This communication is responsive to <u>amdt. of 8-18-06</u> .		
2. The allowed claim(s) is/are <u>1-21</u> .		
 3.	e been received. e been received in Application No cuments have been received in this is of this communication to file a reply of ENT of this application. itted. Note the attached EXAMINER' es reason(s) why the oath or declarate on's Patent Drawing Review (PTO-9 as Amendment / Comment or in the O as Affec) should be written on the drawing the header according to 37 CFR 1.121(c)	national stage application from the complying with the requirements S AMENDMENT or NOTICE OF tion is deficient. 948) attached ffice action of the back) of all.
attached Examiner's comment regarding REQUIREMENT	FOR THE DEPOSIT OF BIOLOGICA	AL MATERIAL.
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5 Distinct of Information	atant Ann Partie
2. ☐ Notice of References Cited (P10-892) Patient Drawing Review (PT0-948)	5. ☐ Notice of Informal Pa6. ☐ Interview Summary	* *
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No./Mail Date 7. ☐ Examiner's Amendm	e
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. Examiner's Stateme 9. Other	nt of Reasons for Allowance
of Biological Material		

Art Unit: 2815

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: the prior art fails to teach the DMOS structure where N devices are laterally arranged where each transistor has a source or drain which surrounds the other source/drain and one of the source/drains is formed unique to each transistor while another source/drain is shared among the plural transistors and where a unique source/drain electrode is formed for each of the double diffused transistors, as claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edward Wojciechowicz whose telephone number is 571-272-1739. The examiner can normally be reached on Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ken Parker can be reached on (571) 272-2298. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application
Information Retrieval (PAIR) system. Status information for published applications may be obtained from
either Private PAIR or Public PAIR. Status information for unpublished applications is available through
Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should
you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC)
at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative
or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-2721000.

Edward Wojciechowicz Primary Examiner Art Unit 2815